Abstracts

An 18 GHz-band MMIC linearizer using a parallel diode with a bias feed resistance and a parallel capacitor

K. Yamauchi, M. Nakayama, Y. Ikeda, H. Nakaguro, N. Kadowaki and T. Araki. "An 18 GHz-band MMIC linearizer using a parallel diode with a bias feed resistance and a parallel capacitor." 2000 MTT-S International Microwave Symposium Digest 00.3 (2000 Vol. III [MWSYM]): 1507-1510.

An 18 GHz-band MMIC linearizer using a parallel diode with a bias feed resistance and a parallel capacitor has been proposed. This linearizer has weak positive gain deviation and its gain deviation can be controlled without changing phase deviation. By applying this linearizer to an 18 GHz-band power amplifier, an improvement of IMD3 of 20 dB has been achieved.

❑ Return to main document.